

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	11648	sense adj amplif\$5 with (enabl\$5 or activat\$5)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/18 11:00
L2	494	1 and inverter with (delay\$5 or control\$5) with (ground or vss or 0v)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/18 11:01
L3	1	1 and inverter with (delay\$5 or control\$5) with (ground or vss or 0v) with off near2 current with (inactiv\$3 or deselect\$5 or unselect\$5)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/18 11:02
L4	2	2 and proportional\$5 with invers\$5 with time	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/18 11:04
L5	3226143	memory or storage	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/18 11:04
L6	105	5 and delay\$5 with off near3 current with transistor	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/18 11:05
L7	21	6 and clock with delay\$5	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/18 11:05
L8	3	6 and clock with delay\$5 with sense adj amplif\$5	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/18 11:10
L9	389	5 and delay\$5 with (dummy or reference) near3 (bit or bitline)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/18 11:12
L10	96	9 and (word or wordline or row) with (ground or vss or 0v)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/18 11:13
L11	7	10 and off near3 current with transistor	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/18 11:14
L12	2	11 and sense adj amplif\$5 with clock with (enabl\$5 or activat\$5 or select\$5)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/18 11:16

L13	243	5 and delay\$5 with gate with (source or drain) with (ground or vss or 0v)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/18 11:17
L14	245	5 and delay\$5 with gate with (source or drain) with (ground or vss or 0v)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/18 11:18
L15	25	14 and sense adj amplif\$5 with clock with (enabl\$5 or select\$5 or activat\$5)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/18 11:41
L16	2307	5 and gate with (drain or source) with (common\$5 or together) with (ground or vss or 0v)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/18 11:45
L17	215	16 and amplif\$5 with clock	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/18 11:44
L18	115	16 and amplif\$5 with clock with (enabl\$5 or select\$5 or activat\$5)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/18 11:45
L19	2	18 and gate with (drain or source) with (common\$5 or together) with (ground or vss or 0v) with (precharg\$5 or pre-charg\$5)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/18 11:45

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L3	0	(sense adj amplifer and enable adj signal and inverter and ground adj voltage and off-current and transistor and inactive and delay\$5 and time and invers\$5 near2 proportional\$5).clm.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/18 12:45
L4	1	((memory or storage) and (voltage or potential or level) and off-current and transistor and clock).clm.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/18 12:48
L5	1	((memory or storage) and dummy adj bit adj line and word adj line and ground and off-current and transistor).clm.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/18 12:49
L6	5	((memory or storage) and precharg\$5 and power and common\$3 and ground and clock and time and sense adj amplifier).clm.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/18 12:50